

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING****CLASS TIME TABLE**

W.E.F: 11/09/2023

Academic Year		Scheme	Semester	Section	Room No	Class Coordinator	Semester Commencement		Last Working Day		
2023-24		2018	VII	A	335	Mrs. Akshata C	11/09/2023		6/1/2024		
Time	8.00 am To 9:00 am	9.00 am To 10.00 am	10.00 am To 10.30 am	10.30 am To 11.30 am	11.30 am To 12.30 pm	12.30 pm To 1.30 pm	1.30 pm To 2.20 pm	2.20 pm To 3.10 pm	3.10 pm To 4.00 pm		
Day											
MON	VLSI	CN	TEA BREAK	RTS	OE(T)	LUNCH BREAK	<----- VLSI Lab A1 / CN Lab A2 ---->				
TUE	RTS	OE		AE	VLSI		<----- VLSI Lab A2 / CN Lab A3 ---->				
WED	CN	RTS		OE	AE(T)		<----- Project Work (A1-A9) ----->				
THU	CN	<----- VLSI Lab A3 / CN Lab A1----->					<----- Project Work (A10-A19) ----->				
FRI	OE	AE		CN(T)	VLSI(T)		LIBRARY		<---- Forum Activity ---->		
SAT	AE	RTS(T)		VLSI	SEMINAR						
Allocation of Subjects								Faculty Mentors			
Subject Name			Subject Code	Course Coordinator		Batch	Faculty Name				
Computer Networks			18EC71	Mrs. Chaitra S N		A1	Mrs. Akshata C				
VLSI Design			18EC72	Dr. Praveen J/Mr. Santhosh B G		A2	Mr. Raviteja B				
Real Time Systems			18EC731	Mr. Suprith P G		A3	Mrs. Chaitra S N				
Automotive Electronics			18EC742	Mrs. Akshata C		Internal Assessment					
Computer Networks Lab			18ECL76	Mrs. Chaitra S N		IA	Date				
VLSI Lab			18ECL77	Mr. Santhosh B G		I	19 th to 20 th Oct 2023				
Project Work Phase-1			18ECP78	Dr. Rajashekar Somasagar		II	23 rd to 25 th Nov 2023				
Open Elective* - Offered by other Departments						III	1 st to 3 rd Jan 2024				

 Coordinator(s)

 H. G. M. Institute of Technology
 DAVANGERE-577 006

 DEAN ACADEMICS
 G.M. Institute of Technology
 DAVANGERE-577006.

 Approved by IQAC
 IQAC Director
 GM Institute of Technology
 Davangere - 577 006.

 PRINCIPAL
 GM Institute of Technology
 Davangere - 577 006.



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CLASS TIME TABLE

W.E.F: 11/09/2023

Academic Year		Scheme	Semester	Section	Room No	Class Coordinator	Semester Commencement		Last Working Day
2023-24		2018	VII	B	342	Hanumanthraju	11/09/2023		6/1/2024
Time	8.00 am To 9:00 am	9.00 am To 10.00 am	10.00 am To 10.30 am	10.30 am To 11.30 am	11.30 am To 12.30 pm	12.30 pm To 1.30 pm	1.30 pm To 2.20 pm	2.20 pm To 3.10 pm	3.10 pm To 4.00 pm
Day									
MON	RTS	AE	TEA BREAK	VLSI	CN	LUNCH BREAK	<----- Project Work (B1-B9) ----->		
TUE	AE	OE		RTS	VLSI(T)		CN(T)	OE(T)	SEMINAR
WED	CN	<----- VLSI Lab B1 / CN Lab B2 ----->					<----- Project Work (B10-B19) ----->		
THU	OE	VLSI		CN	AE		<----- VLSI Lab B2 / CN Lab B3 ----->		
FRI	RTS	<----- VLSI Lab B3 / CN Lab B1 ----->					LIBRARY	<----- Forum Activity ----->	
SAT	VLSI	OE		RTS(T)	AE(T)				

Allocation of Subjects

Faculty Mentors

Subject Name	Subject Code	Course Coordinator	Batch	Faculty Name
Computer Networks	18EC71	Mrs. Veena H T	B1	Mrs. Manjula B K
VLSI Design	18EC72	Dr. Praveen J/ Ms. Deepika V B	B2	Dr. Rajashekhar S
Real Time Systems	18EC731	Mrs. AsSha T	B3	Dr. Kavitha K J
Automotive Electronics	18EC742	Mr. Hanumanthraju R K		Internal Assessment
Computer Networks Lab	18ECL76	Mrs. Asha T	IA	Date
VLSI Lab	18ECL77	Ms. Deepika V B	I	19 th to 20 th Oct 2023
Project Work Phase-1	18ECP78	Dr. Kavitha K J	II	23 rd to 25 th Nov 2023
Open Elective* - Offered by other departments			III	1 st to 3 rd Jan 2024

[Signature]
 Coordinator(s)

[Signature]
 Head of the Department
 Dept. of Electronics & Comm. Engg.
 GM Institute of Technology
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